ELECTRICAL CHARACTERISTICS (V_S = 13.2V; f = 1KHz; R_g = 600Ω; R_L = 4Ω; T_{amb} = 25°C; Refer to the test and application diagram, unless otherwise specified.)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
I_{q1}	Quiescent Current	$R_L = \infty$	120	200	320	mA
V_{OS}	Output Offset Voltage	Play Mode			±60	mV
dV_{OS}	During mute ON/OFF output offset voltage				±60	mV
G_v	Voltage Gain		25	26	27	dB
dG_v	Channel Gain Unbalance				±1	dB
P_o	Output Power	V_S = 13.2V; THD = 10% V_S = 13.2V; THD = 1% V_S = 14.4V; THD = 10% V_S = 14.4V; THD = 1%	23 16 28 20	25 19 30 23		W W W W
		V_S = 13.2V; THD = 10%, 2Ω V_S = 13.2V; THD = 1%, 2Ω V_S = 14.4V; THD = 10%, 2Ω V_S = 14.4V; THD = 1%, 2Ω	42 32 50 40	45 34 55 43		W W W W
$P_{o\ EIAJ}$	EIAJ Output Power (*)	V_S = 13.7V; R_L = 4Ω V_S = 13.7V; R_L = 2Ω	41	45 77		W W
$P_{o\ max.}$	Max. Output Power (*)	V_S = 14.4V; R_L = 4Ω V_S = 14.4V; R_L = 2Ω		50 80		W W
THD	Distortion	P_o = 4W P_o = 15W; R_L = 2Ω		0.006 0.015	0.05 0.07	% %
e_{No}	Output Noise	"A" Weighted Bw = 20Hz to 20KHz		35 50	50 70	µV µV
SVR	Supply Voltage Rejection	f = 100Hz; V_r = 1Vrms	50	70		dB
f_{ch}	High Cut-Off Frequency	P_O = 0.5W	100	300		KHz
R_i	Input Impedance		80	100	120	KΩ
C_T	Cross Talk	f = 1KHz P_O = 4W f = 10KHz P_O = 4W	60	70 60	– –	dB dB
I_{SB}	St-By Current Consumption	$V_{St\text{-}By}$ = 1.5V			75	µA
I_{pin4}	St-by pin Current	VSt-By = 1.5V to 3.5V			±10	µA
$V_{SB\ out}$	St-By Out Threshold Voltage	(Amp: ON)	3.5			V
$V_{SB\ in}$	St-By in Threshold Voltage	(Amp: OFF)			1.5	V
A_M	Mute Attenuation	P_{Oref} = 4W	80	90		dB
$V_{M\ out}$	Mute Out Threshold Voltage	(Amp: Play)	3.5			V
$V_{M\ in}$	Mute In Threshold Voltage	(Amp: Mute)			1.5	V
$V_{AM\ in}$	V_S Automute Threshold	(Amp: Mute) Att ≥ 80dB; P_{Oref} = 4W (Amp: Play) Att < 0.1dB; P_O = 0.5W	6.5	7 7.5	 8	V V
I_{pin22}	Muting Pin Current	V_{MUTE} = 1.5V (Sourced Current)	7	12	18	µA
		V_{MUTE} = 3.5V	-5		18	µA
HSD SECTION						
$V_{dropout}$	Dropout Voltage	I_O = 0.35A; V_S = 9 to 16V		0.25	0.6	V
I_{prot}	Current Limits		400		800	mA
OFFSET DETECTOR SECTION						
V_{M_ON}	Mute Voltage for DC offset detection enabled	V_{stby} = 5V	8			V
V_{M_OFF}					6	V
V_{OFF}	Detected Differential Output Offset	V_{stby} = 5V; V_{mute} = 8V	±2	±3	±4	V
V_{25_T}	Pin 25 Voltage for Detection = TRUE	V_{stby} = 5V; V_{mute} = 8V V_{OFF} > ±4V	0		1.5	V
V_{25_F}	Pin 25 Voltage for Detection = FALSE	V_{stby} = 5V; V_{mute} = 8V V_{OFF} > ±2V	12			V

(*) Saturated square wave output.