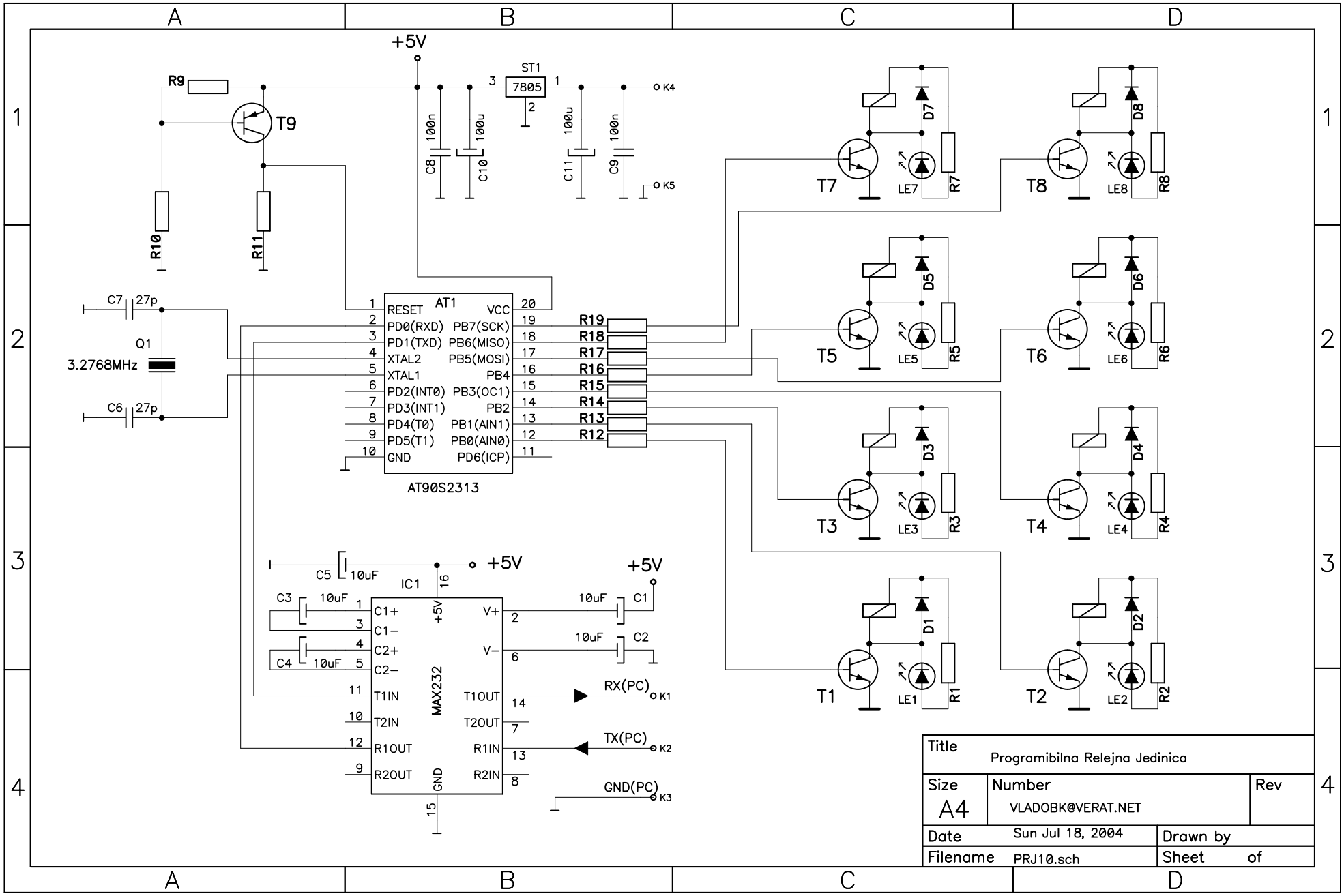
+5V
R9
T9
R10
R11
ST1
7805
K4
K5
C8 100n
C10 100u
C11 100u
C9 100n
C7 27p
C6 27p
Q1
3.2768MHz
AT1
RESET
PD0(RXD)
PD1(TXD)
XTAL2
XTAL1
PD2(INT0)
PD3(INT1)
PD4(T0)
PD5(T1)
GND
VCC
PB7(SCK)
PB6(MISO)
PB5(MOSI)
PB4
PB3(OC1)
PB2
PB1(AIN1)
PB0(AIN0)
PD6(ICP)
AT90S2313
R19
R18
R17
R16
R15
R14
R13
R12
C5 10uF
IC1
C3 10uF
C4 10uF
C1+
C1−
C2+
C2−
T1IN
T2IN
R1OUT
R2OUT
MAX232
V+
V−
T1OUT
T2OUT
R1IN
R2IN
C1 10uF
C2 10uF
RX(PC)
TX(PC)
GND(PC)
K1
K2
K3
T7
T8
T5
T6
T3
T4
T1
T2
D7
D8
D5
D6
D3
D4
D1
D2
LE7
LE8
LE5
LE6
LE3
LE4
LE1
LE2
R7
R8
R5
R6
R3
R4
R1
R2
Title
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Rev
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Sun Jul 18, 2004
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