'\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

'\* Name : UNTITLED.BAS \*

'\* Author : [set under view...options] \*

'\* Notice : Copyright (c) 2016 [set under view...options] \*

'\* : All Rights Reserved \*

'\* Date : 4/13/2016 \*

'\* Version : 1.0 \*

'\* Notes : \*

'\* : \*

'\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

include "modedefs.bas"

@ DEVICE pic12F629, INTRC\_OSC\_NOCLKOUT ; System Clock Options

@ DEVICE pic12F629, WDT\_ON ; Watchdog Timer

@ DEVICE pic12F629, PWRT\_ON ; Power-On Timer

@ DEVICE pic12F629, MCLR\_OFF ; Master Clear Options (Internal)

@ DEVICE pic12F629, BOD\_OFF ; Brown-Out Detect

@ DEVICE pic12F629, CPD\_OFF ; Data Memory Code Protect

@ DEVICE pic12F629, PROTECT\_OFF

DEFINE OSCCAL\_1K 1' Set OSCCAL for 1k

cmcon = 7 ' Comparator OFF

TRISIO = 0 ' Set GPIO 0 as output

pocetak:

 ' a

 gpio=17

 pause 2000

 ' b

 gpio=33

 pause 7000

 ' c

 gpio=1

 pause 1000

 ' d

 gpio=33

 pause 1000

 ' e

 gpio=1

 pause 1000

 ' f

 gpio=33

 pause 1000

 ' g

 gpio=17

 pause 2000

 ' h

 gpio=19

 pause 1000

 ' i

 gpio=20

 pause 10000

 ' j

 gpio=16

 pause 1000

 ' k

 gpio=20

 pause 1000

 ' l

 gpio=16

 pause 1000

 ' m

 gpio=20

 pause 1000

 ' n

 gpio=18

 pause 2000

goto pocetak

 end