

evo kako si ti nacrtao, i vidis da da crveno ima 2 impulsa.

The screenshot displays the 3DSN-ISIS Professional software interface. The main workspace shows a circuit diagram with the following components and connections:

- V5M1 Signal Generator:** Configured with a frequency of 4.000 kHz and an amplitude of 10.0 V. It is connected to the CLK input of a 4013 flip-flop (U5.A).
- 4013 Flip-Flop (U5.A):** A D-type flip-flop with its D input connected to a red square wave generator. Its Q output is connected to the input of a 4011 NAND gate (U1.A).
- 4011 NAND Gate (U1.A):** A two-input NAND gate with both inputs connected to the Q output of the 4013 flip-flop. Its output is connected to the input of a digital oscilloscope.
- Digital Oscilloscope:** Shows four channels (A, B, C, D) with waveforms. Channel A shows a green square wave, Channel B shows a blue square wave, Channel C shows a red square wave, and Channel D shows a purple square wave. The red waveform (Channel C) has two distinct pulses.

The software interface includes a menu bar (File, View, Edit, Tools, Design, Graph, Source, Debug, Library, Template, System, Help), a toolbar, a device list on the left, and a status bar at the bottom showing the current time and CPU load.

ako dodam djelitelj sa 2 dobijem kako treba 4 impulsa, tacno koliko je i $F1/F2$

The screenshot displays the ISIS Professional (Animating) interface. The main workspace shows a digital circuit simulation. At the top, a VSM Signal Generator is configured with a frequency of 1.00K and an amplitude of 10.0. Below it, another VSM Signal Generator is set to a frequency of 4.00K and an amplitude of 12.0. The circuit includes a 4013 flip-flop (U5:A) whose clock input (CLK) is connected to the 4.00K signal. The Q output of the flip-flop is connected to the input of a 4011 NAND gate (U1:A). The output of the NAND gate is connected to the input of a Digital Oscilloscope. The oscilloscope displays four channels: Channel A (blue square wave), Channel B (yellow square wave), Channel C (red square wave), and Channel D (green square wave). The top generator's output is also visible on the oscilloscope. The bottom status bar shows the simulation is running, with a CPU load of 92%.

