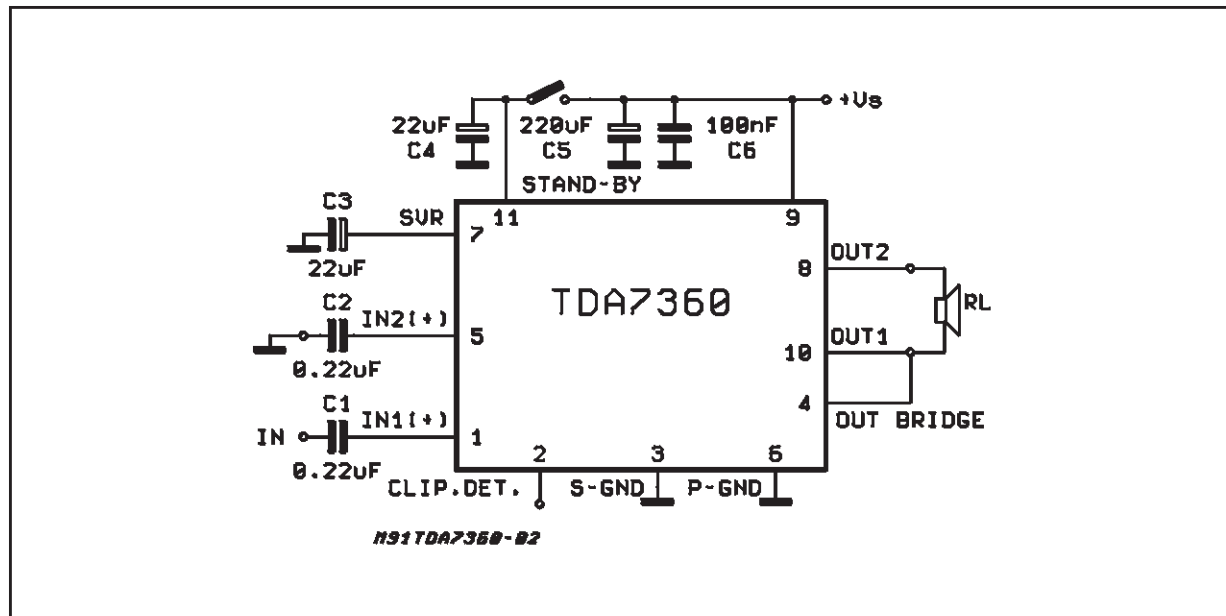


**Figure 3:** BRIDGE Test and Application Circuit



**Figure 4:** P.C. Board and Layout (BRIDGE) of the circuit of fig. 3 (1:1 scale)