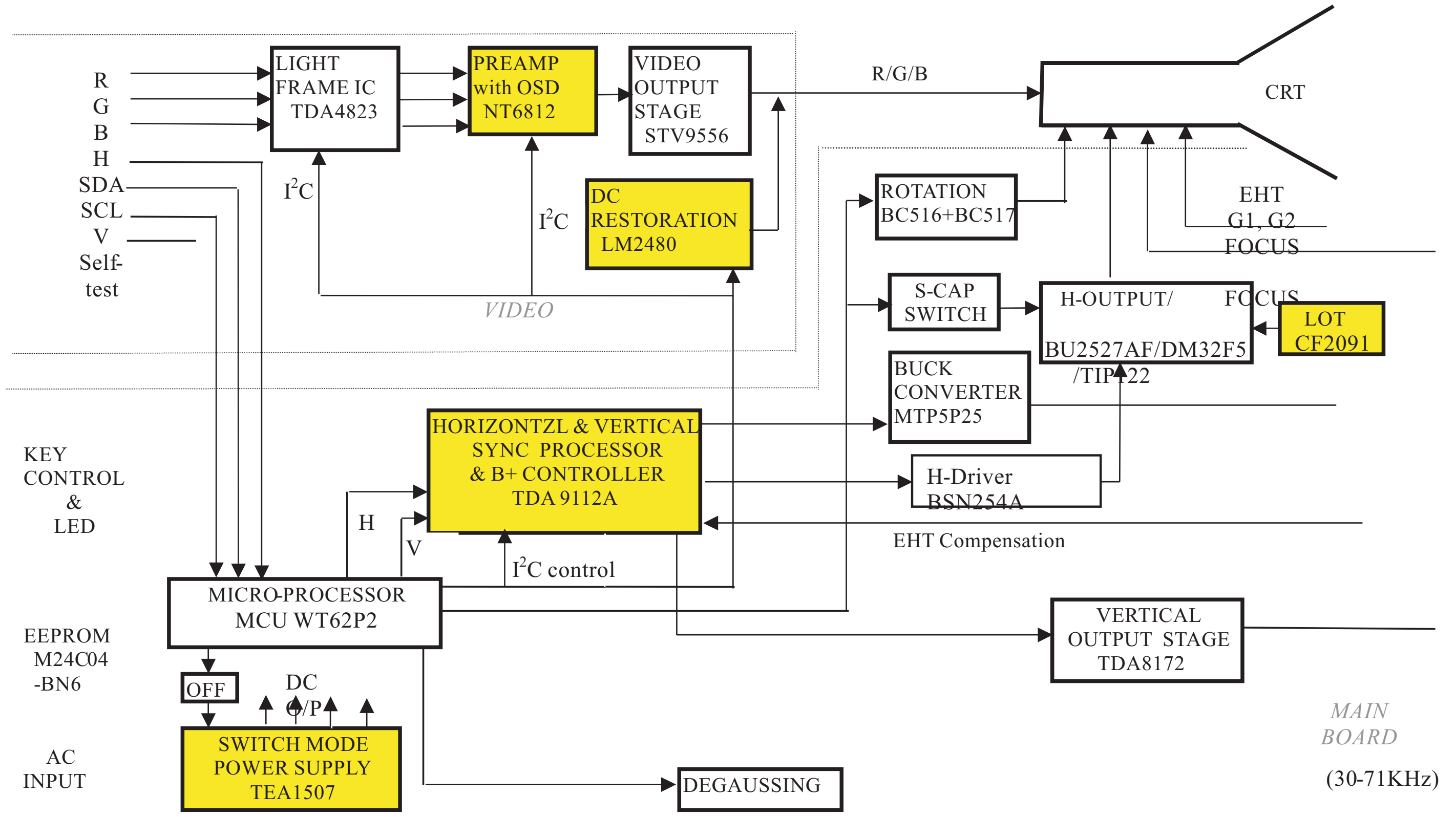
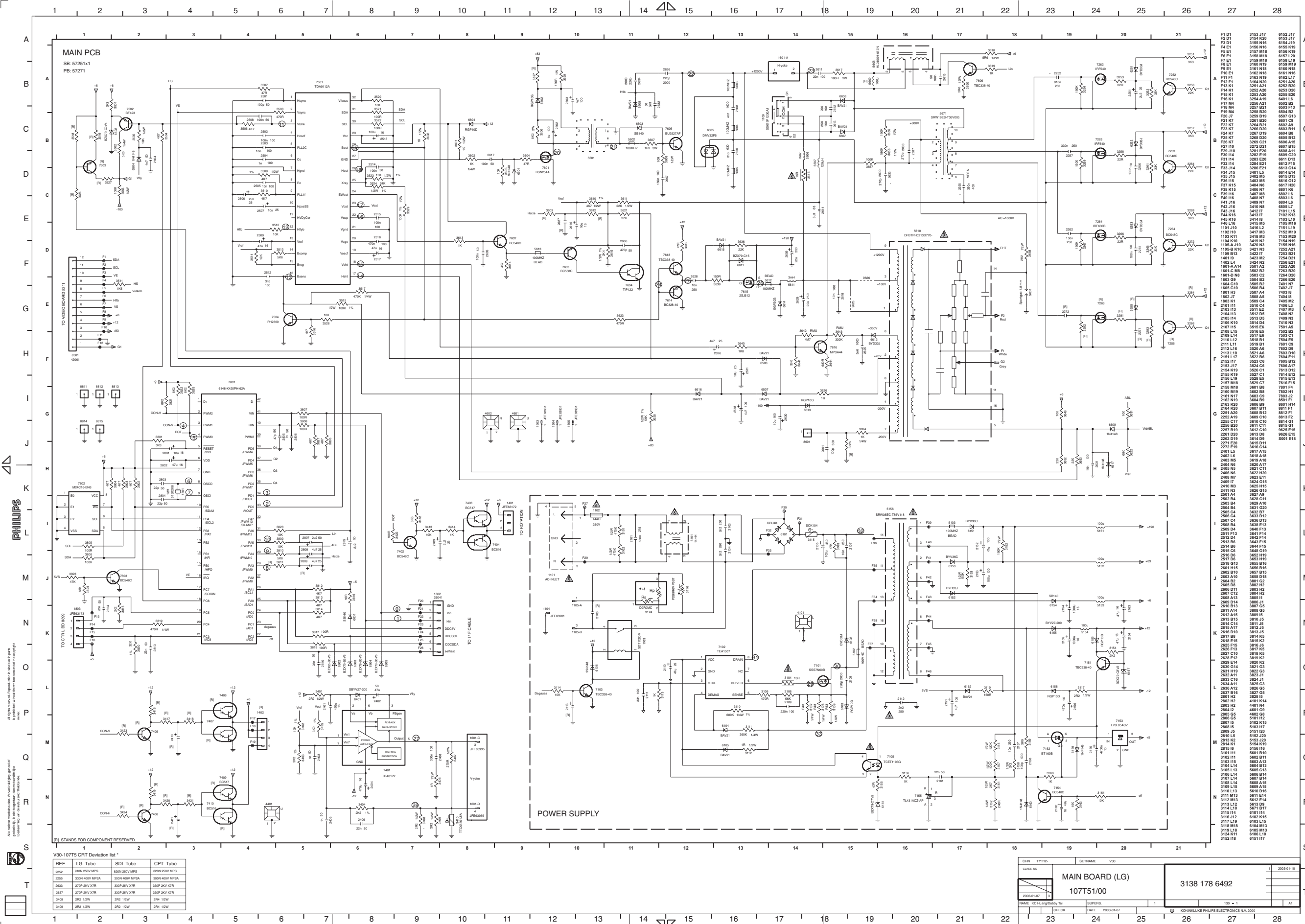


# Function Block of V30 107T5



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# Schematic Diagram of Main board



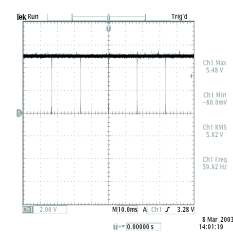
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V30-107T5 CRT Deviation list \*

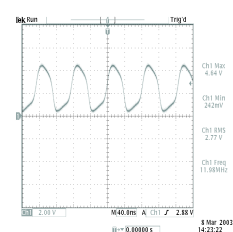
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2252	339N 200V MPTA	339N 200V MPTA	339N 200V MPTA
2253	339N 400V MPTA	339N 400V MPTA	339N 400V MPTA
3033	270P 20V XTR	330P 20V XTR	330P 20V XTR
3037	270P 20V XTR	330P 20V XTR	330P 20V XTR
3408	2R2 1.0W	2R2 1.0W	2R2 1.0W
3409	2R2 1.0W	2R2 1.0W	2R2 1.0W

CHK	TYTD	SETNAME	V30
CLASS NO	MAIN BOARD (LG)		300301-10
DATE	107T5/00		3138 178 6492
NAME	NO	DATE	2003-01-07
CHECK	DATE	NO	1
ROKINLANS PHILIPS ELECTRONICS N.V. 2003			A1

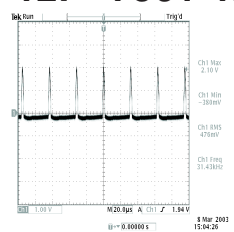
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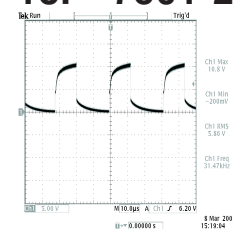
6. 7801-8



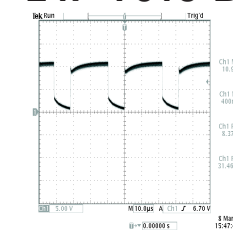
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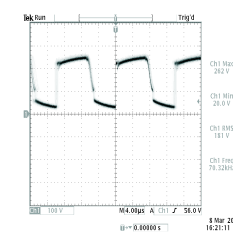
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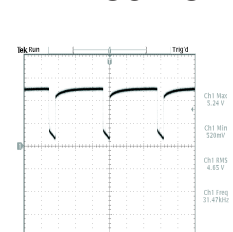
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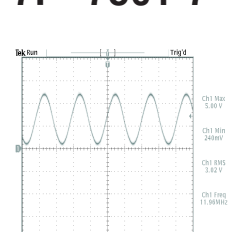
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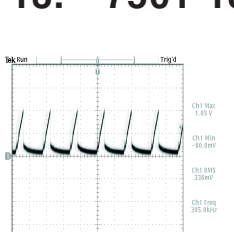
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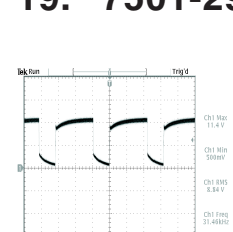
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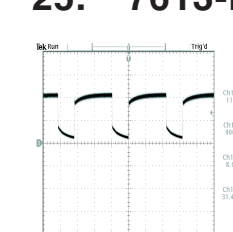
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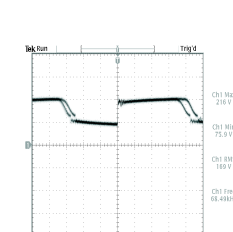
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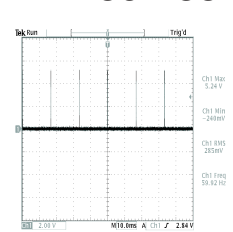
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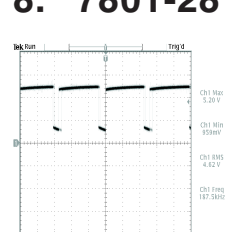
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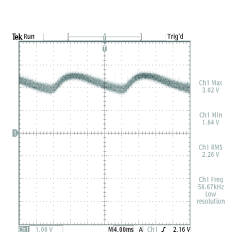
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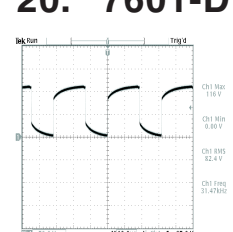
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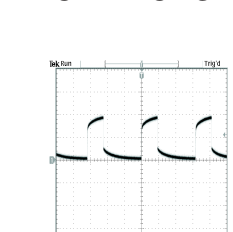
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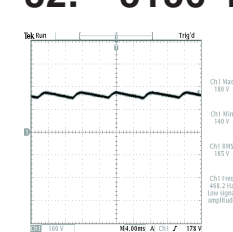
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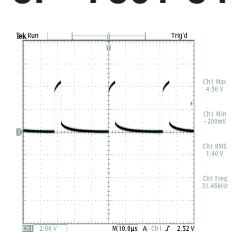
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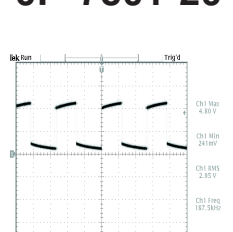
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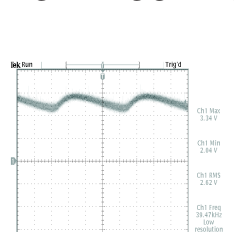
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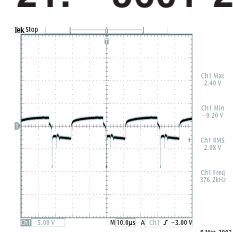
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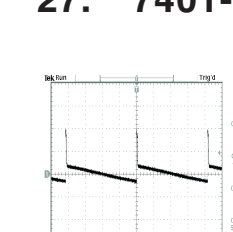
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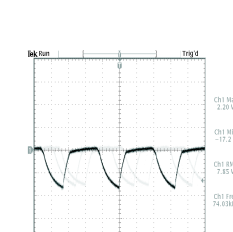
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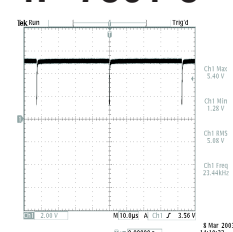
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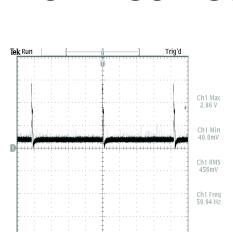
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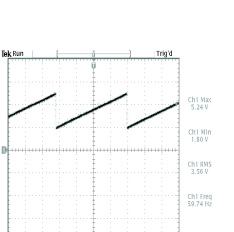
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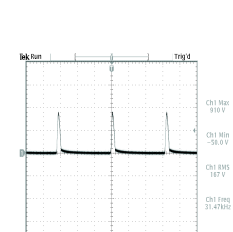
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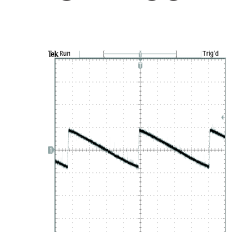
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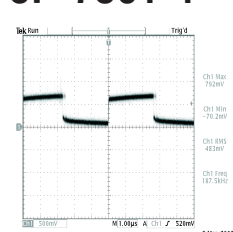
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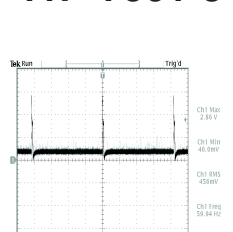
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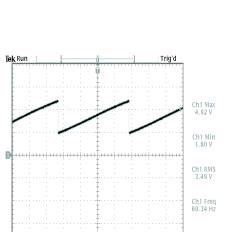
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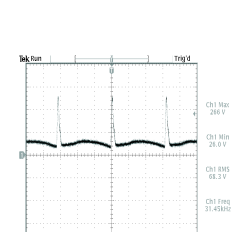
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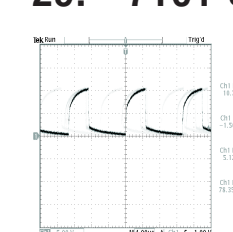
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23. 1601-A-2

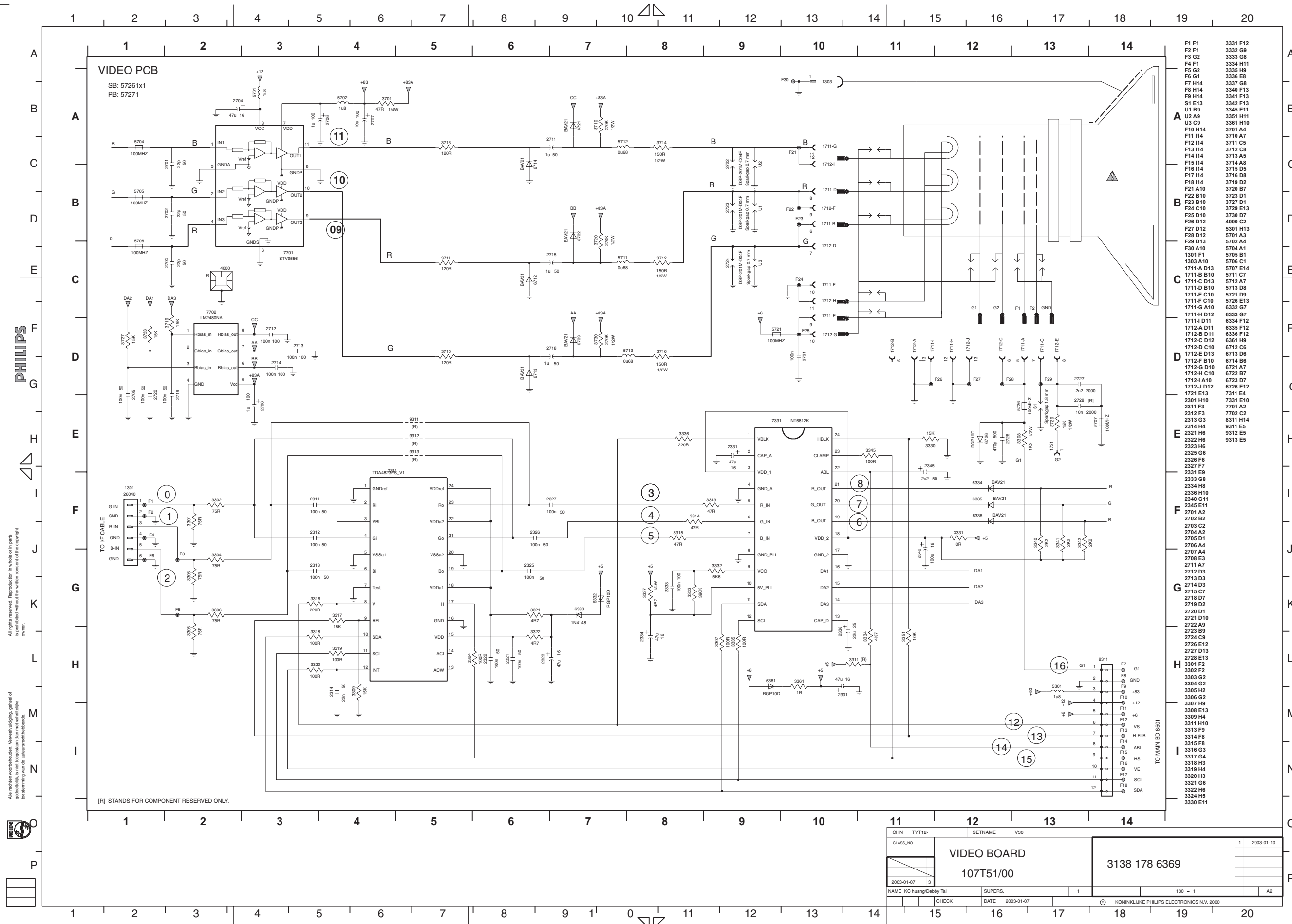


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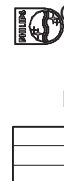
# Schematic Diagram of Video board

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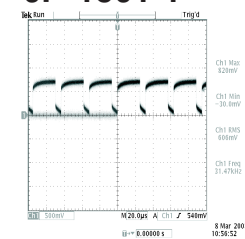
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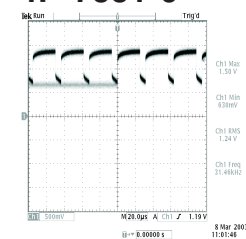


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		107T51/00	
		3138 178 6369	
NAME	KC Huang/Debbay Tai	SUPERS.	1
CHECK		DATE	2003-01-07
			130 - 1
			KONINKLIJKE PHILIPS ELECTRONICS N.V. 2000

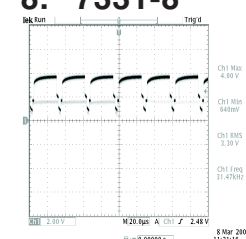
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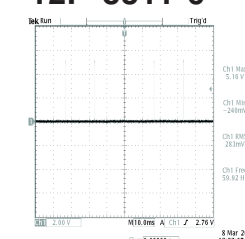
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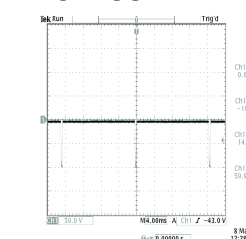
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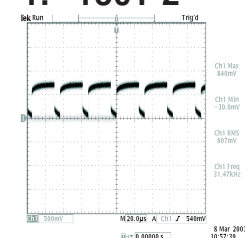
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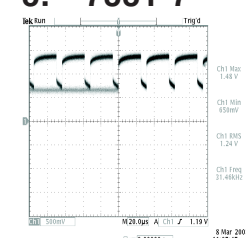
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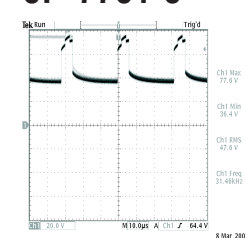
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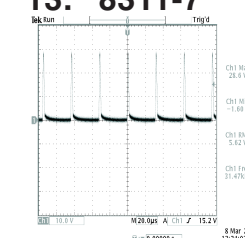
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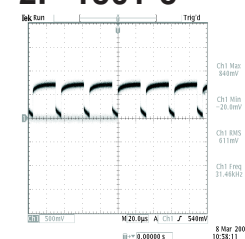
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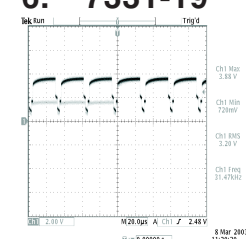
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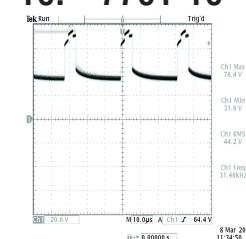
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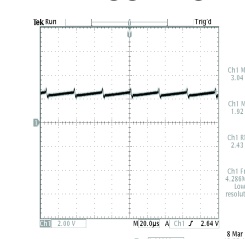
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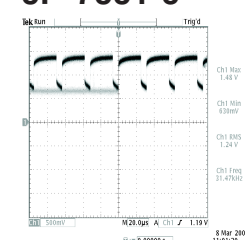
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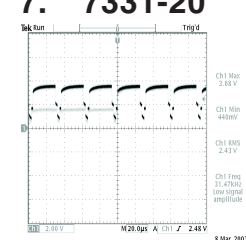
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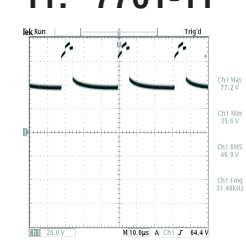
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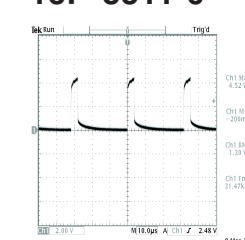
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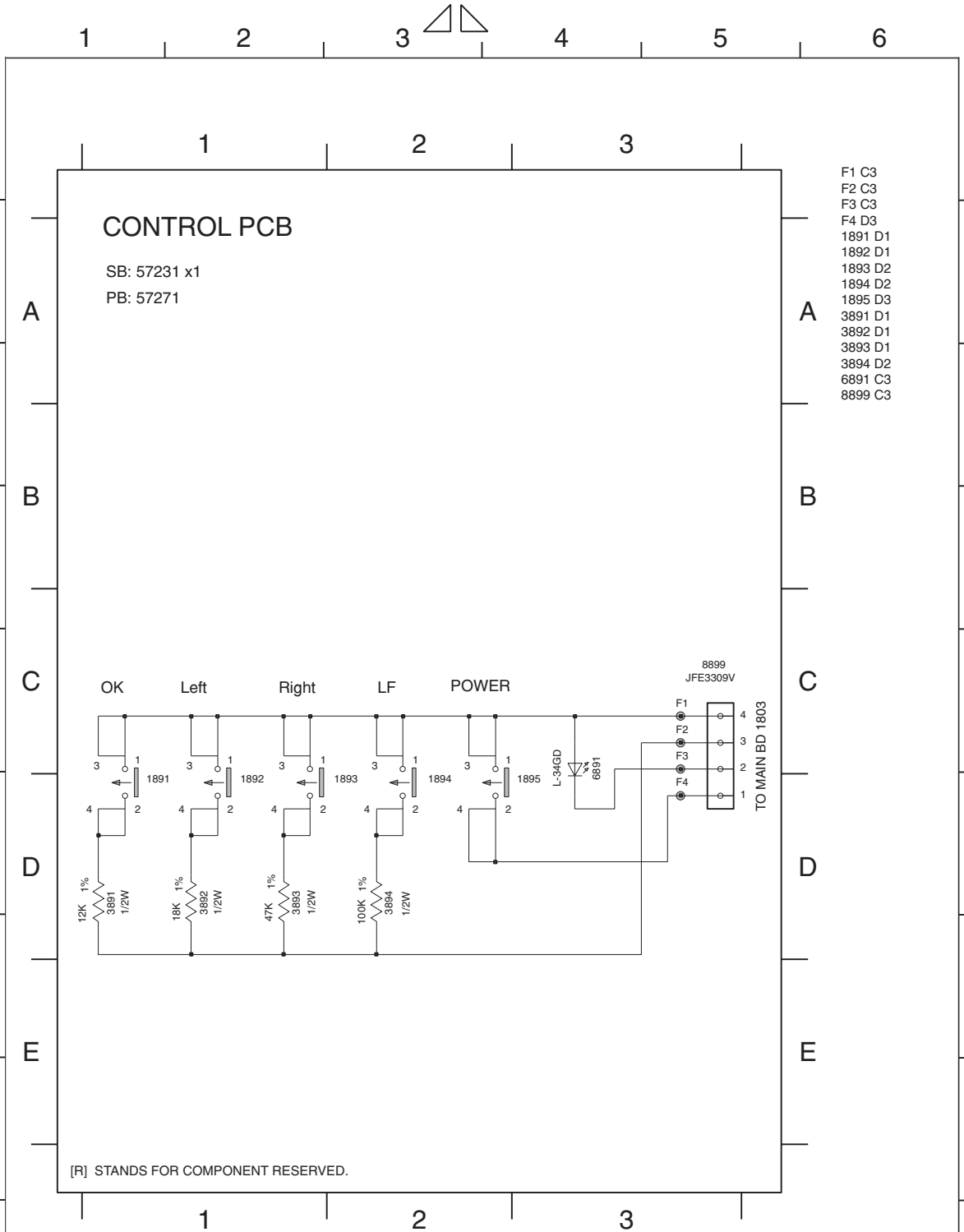
15. 8311-9



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[R] STANDS FOR COMPONENT RESERVED.

CHN	TYT12-	SETNAME	V30
CLASS_NO	CONTROL BOARD		1 2003-01-03
	107T51/00		3138 178 6374
NAME	Roland/Ivy Su	SUPERS.	1 130 - 1 A4
CHECK	DATE	2003-01-03	© KONINKLIJKE PHILIPS ELECTRONICS N.V. 2000