L9308



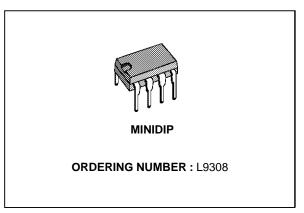
DUAL LOW SIDE DRIVER

- DARLINGTON OUTPUT STAGE
- INPUT COMPARATOR WITH WIDE RANGE COMMON MODE OPERATION AND GROUND COMPATIBLE INPUTS
- INPUT COMPARATOR HYSTERESIS
- SHORT CIRCUIT PROTECTION OF OUTPUT WITH SOA PROTECTION
- INTERNAL THERMAL PROTECTION WITH HYSTERESIS
- SINGLESUPPLYVOLTAGEFROM3.5VUPTO 28V

DESCRIPTION

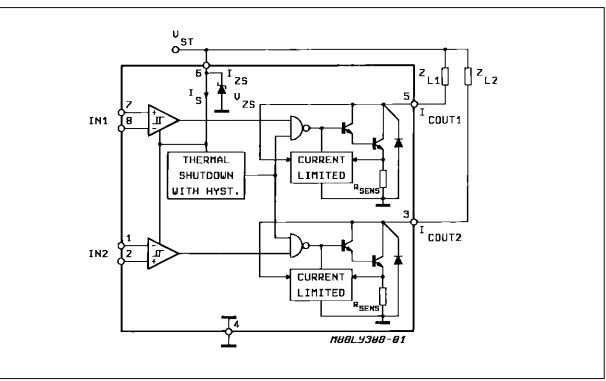
The L9308 is a monolithic interface circuit with differential input comparator and open collector output able to sink current specifically to drive lamps, relays, d.c. motors, electro valves etc.

Particular care has been taken to protect the device against destructive failures - short circuit of outputs to V_S , SOA protection, supply overvoltage.



A built in thermal shut-down switches off the device when the IC's internal dissipation becomes too high and the chip temperature exceeds the security threshold.

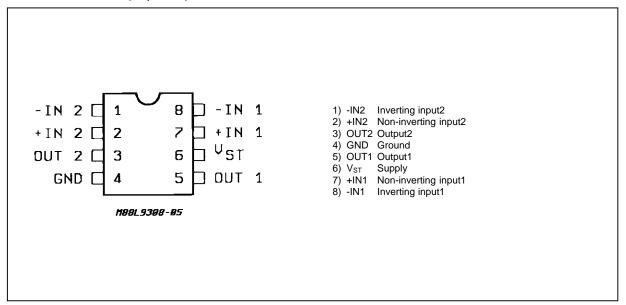
The input comparator hysteresis increases the interface's noise immunity allowing the correct use in critical environments as automotive applications.



BLOCK DIAGRAM

L9308

PIN CONNECTION (Top view)



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit	
I _{ZS}	Current Into Supply Clamp Zener Diode	Tamb = 25°C, DC Pulsed (*)	30 80	mA mA
Vs	Supply Voltage		28	V(**)
lo	Output Current		Internally Limited	
T _j , T _{stg}	Junction and Storage Temperature		– 55 to +150	°C
Ptot	Power Dissipation at Tamb = 85°C		650	mW

(*) TON \leq 2.5ms ; repetition time > 30ms.

(**) The maximum allowed supply voltage without limiting resistor is limited by the built-in protection zener diode : see Vzs spec.

values. If V_{S} is higher than V_{ZS} a resistor R_{S} is necessary to limit the zener current $I_{\text{ZS}}.$

THERMAL DATA

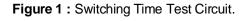
Symbol	Parameter	Value	Unit
R _{th j-amb}	Thermal Resistance Junction-ambient Max	100	°C/W



Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
VIH	Hysteresis of the Input Comparater	$V_{IN} = 200 \text{mVpp}$; f = 1kHz	20		80	mV
lв	Input Bias Current	$V_{I}^{+} = V_{I}^{-} = 0$		0.2	1.0	μA
los	Input Offset Current	$V_{l}^{+} = V_{l}^{-} = 0$		± 50	± 400	nA
CMR	Input Common Mode Range	$V_{s} = 6 - 18V$ $T_{amb} = 25^{\circ}C$	0		V _{ST} -1.6	V
I _{SC}	Output Short Circuit Current for Each Channel (see fig. 4)	$\label{eq:VIN} \begin{split} V_{IN} &- V_{IN} > 70 mV \\ V_S &= 16V \\ T_{amb} &= 25^\circ C \text{ to } 85^\circ C \\ T_{amb} &= -40^\circ C \text{ to } 25^\circ C \\ V_{OUT 1, 2} &= 6V \end{split}$			0.6 0.7 1.2	A A A
V _{CSAT}	On Status Saturation Voltage	$T_{amb} = -40^{\circ}C \text{ to } 25^{\circ}C$ $V_{I}^{-} - V_{I}^{+} > 70mV$ $I_{OUT 1, 2} = 300mA$ $T_{amb} = 25^{\circ}C \text{ to } 85^{\circ}C$		1.0	1.5 1.4	V V
lol	Output Leakage Current	$V_{I}^{-} - V_{I}^{-} > 70mV$ $V_{S} = 18V$ $V_{S} = 5V$		10	300 20	μΑ μΑ
V _{ST}	Supply voltage (pin 6)		3.5		18	V
I "st.by"	Supply Current	$V_{I}^{+} V_{I}^{-} > 70 \text{mV}$		5	8	mA
I _{"ON"}	Supply Current	$V_{l}^{-} V_{l}^{+} > 70 mV$		18		mA
Vzs	Voltage Clamp Supply Protection	I _{ZS} = 10mA	20		27	V
I _{Omin}	Minimum Output Current wiyth the Outputs connected Together	V _{CSAT} = 1.5V	400			mA
t _r t _f	Rise Time (see fig. 2) Fall Time	I _{OUT} = 50mA T _{amb} = 25°C			2 2	μs
t _{don} t _{doff}	Delay Time on Delay Time off	$I_{OUT} = 50mA$ $T_{amb} = 25^{\circ}C$			10 10	μs

$\textbf{ELECTRICAL CHARACTERISTICS} \ (V_s = 14.4V; -40^{\circ}C \leq T_{amb}, \ \leq 85^{\circ}C; \ R_S = 100\Omega \ unless \ otherwise \ noted)$





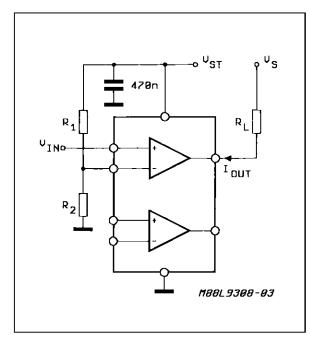


Figure 3 : Typical Application and Test Circuit.

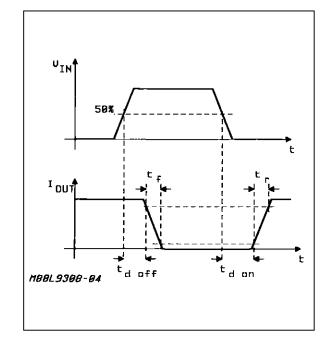


Figure 2 : Switching Time Waveforms for Resistive Loads.

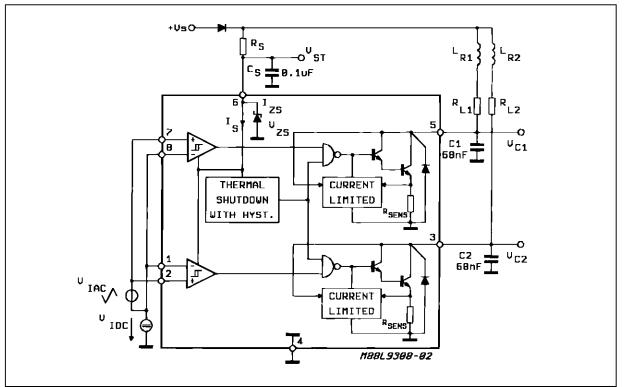
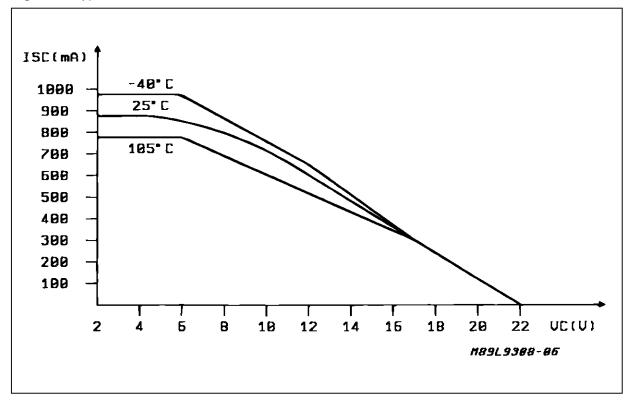




Figure 4 : Typical SOA Characteristic.

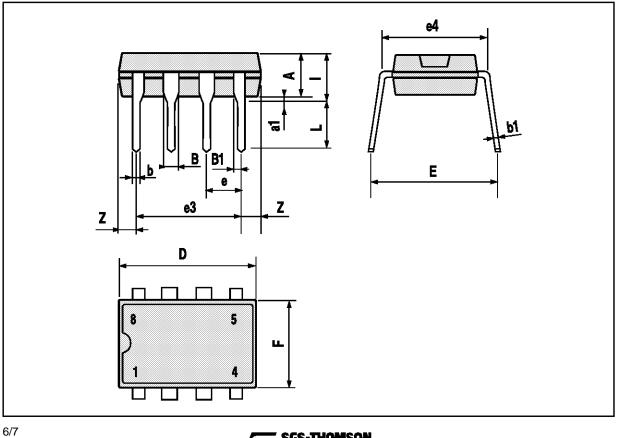




L9308

MINIDIP PACKAGE MECHANICAL DATA

DIM.	mm		inch			
Dim	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А		3.32			0.131	
a1	0.51			0.020		
В	1.15		1.65	0.045		0.065
b	0.356		0.55	0.014		0.022
b1	0.204		0.304	0.008		0.012
D			10.92			0.430
E	7.95		9.75	0.313		0.384
е		2.54			0.100	
e3		7.62			0.300	
e4		7.62			0.300	
F			6.6			0.260
I			5.08			0.200
L	3.18		3.81	0.125		0.150
Z			1.52			0.060





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